

Notice of Allowability	Application No.	Applicant(s)	
	10/645,729	MOHANTY ET AL.	
	Examiner	Art Unit	
	Suchin Parihar	2825	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to after-final amendment filed 1/8/2007.
2. The allowed claim(s) is/are 1-5,7-9 and 11-20.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some* c) None of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application
6. Interview Summary (PTO-413),
Paper No./Mail Date *same as date of this action*
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

PAUL DINH
PRIMARY EXAMINER

DETAILED ACTION

Drawings

1. The drawings filed on 8/22/2003 are acceptable subject to correction of the informalities indicated on the attached "Notice of Draftsperson's Patent Drawing Review," PTO-948. In order to avoid abandonment of this application, correction is required in reply to the Office action. The correction will not be held in abeyance.

EXAMINER'S AMENDMENT

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

3. Authorization for this examiner's amendment was given in a telephone interview with Jayanthi Minisandram (Reg. #53,330) on 1/18/2007.

The application has been amended as follows:

In the claims

4. **In claim 1 line 7**, after "tasks;," insert on a separate line - -executing the HDL based tasks on a model of an integrated circuit design associated with the simulation server;- -.

In claim 1, line 8, delete "executing the test case on the pre-initialized simulation server;"

In claim 9, line 9, after “tasks;”, insert on a separate line - -executing the HDL based tasks on a model of an integrated circuit design associated with the server;- -.

In claim 9, line 10, delete “executing the test case and ”.

In claim 14, line 10, after “verified file”, insert - -on a model of an integrated circuit design associated with the server- -.

Cancel claims 6 and 10.

With respect to the cancellation of claims 6 and 10, the subject matter of these claims are now incorporated into claims 1 and 9. Therefore, dependent claims 6 and 10 no longer further limit the subject matter of their parent claims 1 and 9 respectively.

(This Examiner's amendment has been made in order to place the application in a condition for allowance)

Reasons For Allowance

5. The following is an examiner's statement of reasons for allowance:
6. Claims 1-5, 7-9 and 11-20 are allowed because the prior art made of record does not teach or suggest a method/system/computer-readable-medium for submitting a test case for simulation of an integrated circuit design as claimed in claims 1, 9 and 14 for the reasons as outlined below.
7. Claims 1-5, 7-9 and 11-13 are allowed because the prior art made of record fails to teach in particular the following steps:

associating text file based tasks of the test case to hardware description language (HDL) based tasks;

executing HDL based tasks on a model of an integrated circuit design associated with the simulation server; and

executing a reset and initialization sequence at the pre-initialized simulation server [i.e. resetting the server] to maintain the pre-initialized simulation server in an initialized state for a next test case.

8. Claims 14-20 are allowed because the prior art made of record fails to teach in particular the following steps:

wherein the verified file includes text based tasks associated with hardware description language (HDL) based tasks and

wherein after execution of the HDL based tasks of the verified file on a model of an integrated circuit design associated with the server, the server is enabled to communicate results to the client and the server resets to the initialized state.

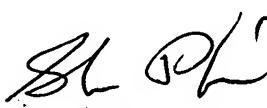
Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Suchin Parihar whose telephone number is 571-272-6210. The examiner can normally be reached on Mon-Fri, 8:30am-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jack Chiang can be reached on 571-272-7483. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status

information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.


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